



WLCSP6_3-2

wafer level chip-size package; 6 bumps (3 x 2)

22 June 2017

Package information

1. Package summary

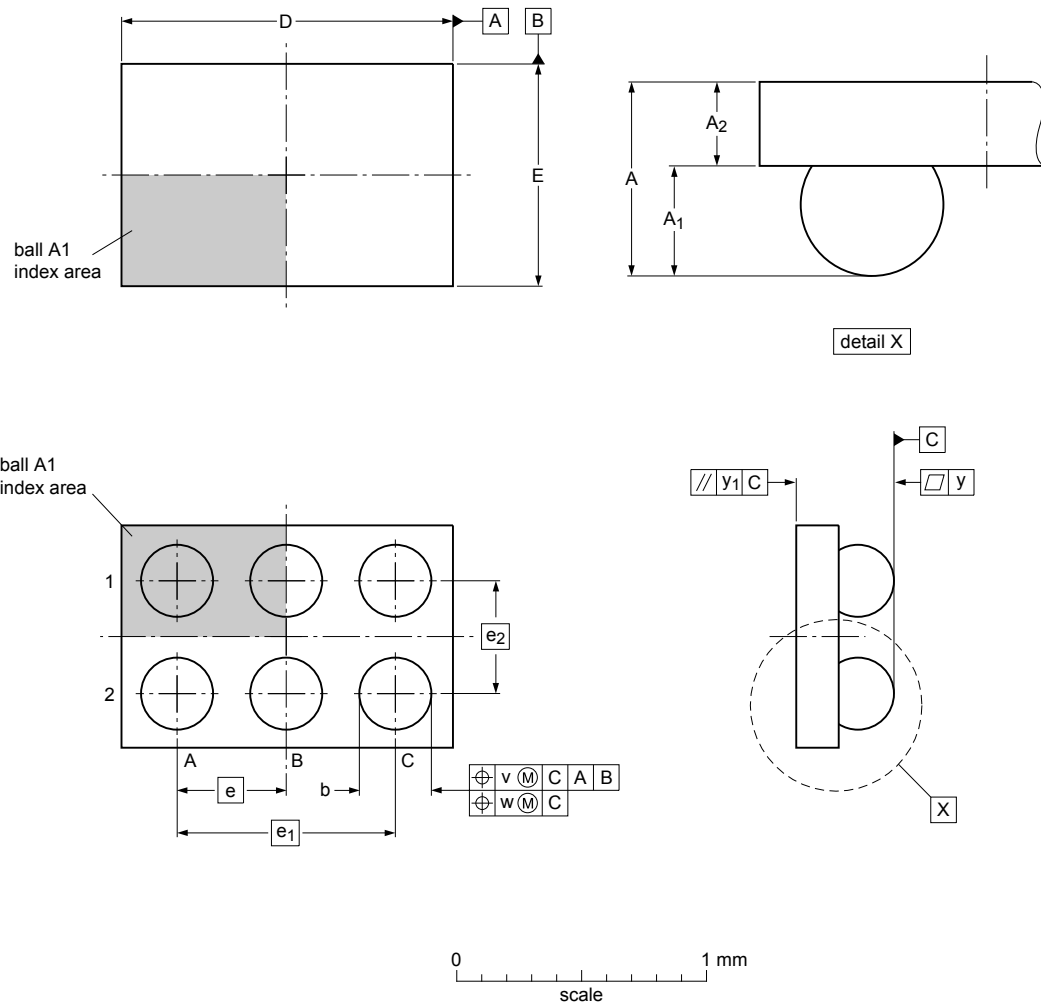
Terminal position code	B (bottom)
Package type descriptive code	WLCSP
Package type industry code	WLCSP6
Package style descriptive code	UC (uncased chip)
Package body material type	X (other)
Mounting method type	S (surface mount)
Issue date	13-4-2017
Manufacturer package code	WLCSP6

Table 1. Package summary

Symbol	Parameter	Min	Typ	Nom	Max	Unit
D	package length	1.45	-	1.48	1.51	mm
E	package width	0.95	-	0.98	1.01	mm
A	seated height	0.315	-	0.345	0.375	mm
A ₂	package height	0.13	-	0.145	0.16	mm
e	nominal pitch	-	-	0.5	-	mm
n ₂	actual quantity of termination	-	-	6	-	

2. Package outline

WLCSP6: wafer level chip-size package; 6 bumps (3 x 2) WLCSP6_3-2



Dimensions (mm are the original dimensions)

Unit	A	A ₁	A ₂	b	D	E	e	e ₁	e ₂	v	w	y
max	0.375	0.215	0.160	0.275	1.51	1.01						
nom	0.345	0.200	0.145	0.260	1.48	0.98	0.50	1.00	0.50	0.15	0.05	0.05
min	0.315	0.185	0.130	0.245	1.45	0.95						

Note

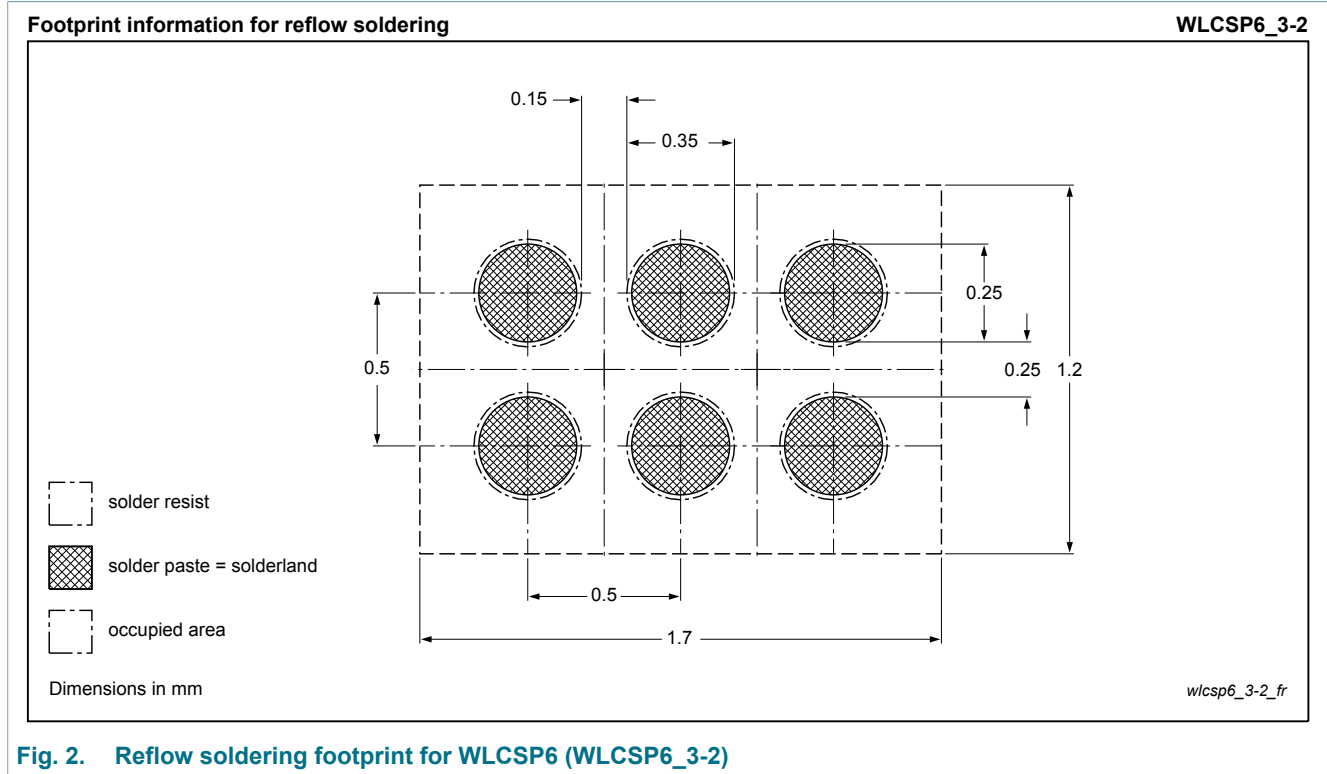
Device back is metal coated on Drain potential.

wlcsp6_3-2_po

Outline version	References			European projection	Issue date
	IEC	JEDEC	JEITA		
WLCSP6_3-2					-17-04-26- 17-05-03

Fig. 1. Package outline WLCSP6 (WLCSP6_3-2)

3. Soldering



4. Legal information

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